

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No: To Be Assigned
Applicant: Kiyotaka Mori
Filed: Herewith
Title: COMBINATION INSULATOR AND ORGANIC SEMICONDUCTOR FORMED
FROM SELF-ASSEMBLING BLOCK CO-POLYMERS
TC/A.U.: To Be Assigned
Examiner: To Be Assigned
Docket No.: MATB-406US

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and to the duty of disclosure set forth in 37 C.F.R. § 1.56, the Examiner in charge of the above-identified application is requested to consider and make of record the references listed on the PTO 1449 (RP) submitted herewith. A copy of each of the listed references is also enclosed.

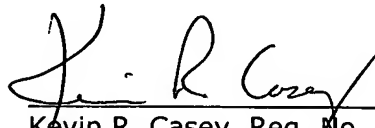
Although the information submitted herewith may be "material" to the Examiner's consideration of the subject application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

Reference JP 2003-92411 A is not in the English language, but an English abstract thereof is attached to the copy of the reference submitted herewith.

This Information Disclosure Statement is being filed concurrently with the above-referenced application.

Respectfully submitted,



Kevin R. Casey, Reg. No. 32,117
Attorney(s) for Applicant(s)

KRC/tmb

Enclosures: PTO Form 1449
(3) References

Dated: March 26, 2004

P.O. Box 980
Valley Forge, PA 19482
(610) 407-0700

The Commissioner for Patents is hereby
authorized to charge payment to Deposit
Account No. 18-0350 of any fees associated
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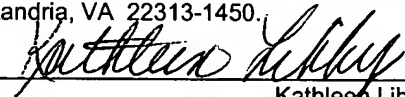
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March 26, 2004

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Kathleen Libby

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Substitute for Form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	To Be Assigned
Filing Date	Herewith
First Named Inventor	Kiyotaka Mori
Art Unit	To Be Assigned
Examiner Name	To Be Assigned
Attorney Docket No.	MATB-406US

SHEET 1 of 2

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

¹Applicant's unique citation designation number (optional).

²See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3).

⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

⁶Applicant is to place a check mark here if English language translation is attached.

The collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	To Be Assigned
Filing Date	Herewith
First Named Inventor	Kiyotaka Mori
Art Unit	To Be Assigned
Examiner Name	To Be Assigned
Attorney Docket No.	MATB-406US

SHEET 2 of 2

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		AIST Announces World's Thinnest Vertical-Type Double-Gate MOSFET Using Newly Discovered Process, AIST Today International Edition No. 8, pp. 10-12, 2003.	<input type="checkbox"/>
		MASSIMO LAZZAIR ET AL., Block Copolymers as a Tool for Nanomaterial Fabrication, Advanced Materials 15, No. 19, pp. 1583-1594, October 2, 2003.	<input type="checkbox"/>
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